

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	7	"5917764".pn. or "6415403".pn. or "6085334".pn. or "6445627".pn. or "5337318".pn. or "6363008".pn. or "20010030896"	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:01
L3	3	I2 and switch	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:02
L4	67	(setting adj (memory or circuit)) and repair with memory	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:20
L5	8	(self with repair) and (serial with scan)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:20
L6	26	((self with repair) and (chain with scan)) and(not ((self with repair) and (serial with scan)))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:21
L7	7	(setting adj memory) and (self with repair)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:21
L8	23	(714/710.ccls. and @ad<"20011220") and (processor or (logic adj unit)) and (setting with (circuit or memory))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:21
L9	2693	setting adj memory	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:21
L10	700	L9 and (process\$3 adj unit)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:21
L11	10	L10 and ((repair\$4 or replac\$3 or substitut\$3) with (redundant or spare) with (element or bit or word or row or column))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:21
L12	15	(setting adj memory) and repair with memory and fus\$4	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:22
L13	477	"365/185.05".ccls.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:23
L14	3	L13 and (fus\$3 with repair)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:23
L15	139	I14 or I12 or I11 or I8 or I7 or I6 or I5 or I4 or I2	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:24

L16	24	I15 and (switch).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:37
L17	49	((microprocessor or (data adj process\$3)) and (fus\$1) and (setting adj memory))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:23
L18	42	I17 and switch	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:47
L19	301	(kaiser-r\$ or schamberger-f\$).in.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:56
L20	3889	infineon.as.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:56
L21	4164	I19 or I20	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 13:56
L22	18	I21 and (data adj processing adj unit).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:02
L23	438	I21 and (switch).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:02
L25	64	I23 and ((register or latch) with switch)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:09
L26	2	I25 and @pd<"20001220"	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:04
L27	554859	(latch or register or (flip adj flop) or (scan adj chain) or LSSD or bsc or (scan adj cell))	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:10
L28	63103	I27 with switch	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:11
L29	17595	I28 and memory.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:11
L30	299	I29 and (setting adj memory)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:12
L31	240	I30 and @ad<"20001220"	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:12
L32	143	I31 and (register with switch)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:14

L33	43	I32 and (setting adj memory).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:17
L34	2104	(self adj test\$3).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:18
L35	8	I34 and (setting adj memory).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:18
L36	11	I17 and switch.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:25
L37	885792	process\$3.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:26
L38	32984	I37 and buffer.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:26
L39	3589	I38 and setting.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:26
L40	6363	I29 and register.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:26
L41	2711	I40 and switch.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:27
L42	217	I41 and (switch with buffer).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:29
L43	4	I42 and fus\$1.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:30
L44	1497	I41 and (switch with register).clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:29
L45	19	I44 and fus\$1.clm.	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/08/02 14:30